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SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY:: PUTTUR
(AUTONOMOUS)

B.Tech II Year II Semester Regular and Supplementary Examinations May 2019

COMPUTER ORGANIZATION

(CSE,CSIT)

Time: 3 hours

Max. Marks: 60

(Answer all Five Units **5 x 12 = 60** Marks)

UNIT-I

- 1 a Write about the Functional Units of Computer with neat diagram? 8M
b What is Assembly Language and Write Assembly Language Instruction? 4M

OR

- 2 a Explain about Instruction Cycle with neat diagram? 7M
b Write about the Basic Operational Concepts with neat diagram? 5M

UNIT-II

- 3 a Explain about Floating point numbers, its operations and implementation? 8M
b Explain the techniques in Fast Multiplication with example? 4M

OR

- 4 a Explain in detail about decimal arithmetic unit? 6M
b Draw the H/W Flowchart with an example? 6M

UNIT-III

- 5 a Explain in details about all 3 types of Shift Register Operations? 8M
b Explain about the way of constructing a 4 line common bus system using multiplexers with a neat diagram? 4M

OR

- 6 a Write about Bus transfer with neat diagram? 7M
b Write out Register Representations and way it is used? 5M

UNIT-IV

- 7 a Explain about ROM types? 6M
b Explain about RAM and its types in detail? 6M

OR

- 8 a What is Main Memory and what are the types in it, Explain in detail? 8M
b Explain about Memory Management Requirements? 4M

UNIT-V

- 9 a Explain about Multiprocessor and its classification in detail? 7M
b Explain the concept of Pipelining with clear example with neat sketch? 5M

OR

- 10 a Explain about Inter Processor Arbitration with neat sketch. 6M
b Explain about Parallel Processing and types? 6M

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